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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/605,585	10/10/2003	Masaki Takaoka	RHM-US020052	2584
GLOBAL IP COUNSELORS, LLP 1233 20TH STREET, NW, SUITE 700 WASHINGTON, DC 20036-2680			EXAMINER	
			NADAV, ORI	
			ART UNIT	PAPER NUMBER
			2811	
			MAIL DATE	DELIVERY MODE
			12/05/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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_ i	Application No.	Applicant(s)			
	10/605,585	TAKAOKA ET AL.			
Office Action Summary	Examiner	Art Unit			
	Ori Nadav	2811			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the	correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filled after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filled, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).					
Status					
3) Since this application is in condition for allowan closed in accordance with the practice under E	action is non-final.				
Disposition of Claims	·				
4) ☐ Claim(s) 1 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or					
Application Papers					
9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the december of Replacement drawing sheet(s) including the correction. 11) The oath or declaration is objected to by the Examiner.	epted or b) objected to by the drawing(s) be held in abeyance. See on is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s) Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate			

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 1 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Since applicant does not claim a thin portion comprises selective oxide, the claimed limitations of a non-layered semiconductor substrate comprising a selective oxide film, as recited in claim 1, are unclear as to whether the semiconductor substrate is a non-layered semiconductor substrate or the semiconductor substrate comprising a layer of a selective oxide film.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1, as best understood, is rejected under 35 U.S.C. 102(b) as being anticipated by Kawabuchi (4,451,544).

Kawabuchi teaches in figure 4F and related text a semiconductor device, comprising:

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a unitary and non-layered semiconductor substrate 21', 22, 23' comprising a thin portion 23' that is thinner than adjacent portions of the semiconductor substrate, and a recessed portion formed below the thin portion, the thin portion being configured and arranged to bridge both sides of the recessed portion and strengthen the semiconductor substrate with respect to forces applied from both sides of the semiconductor substrate (inherent);

wherein the thin portion 23' is a selective oxide film and the etching rate of the thin portion is slower than that of the surrounding portions of the semiconductor substrate.

at least one through hole is formed in the thin portion that extends from the recessed portion, and entirely through the thin portion to the upper surface of the semiconductor substrate.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 1 is rejected under 35 U.S.C. 103(a) as obvious over Loschner et al. (5,672,449) in view of Lee et al. (6,028,330).

Loschner et al. teach in figure 5 and related text a semiconductor device, comprising:

a unitary and non-layered semiconductor substrate 1 comprising a thin portion that is thinner than adjacent portions of the semiconductor substrate, and a recessed portion formed below the thin portion, the thin portion being configured and arranged to bridge both sides of the recessed portion and strengthen the semiconductor substrate with respect to forces applied from both sides of the semiconductor substrate (inherent);

wherein the thin portion is a selective oxide film and the etching rate of the thin portion is slower than that of the surrounding portions of the semiconductor substrate,

at least one through hole is formed in the thin portion that extends from the recessed portion, and entirely through the thin portion to the upper surface of the semiconductor substrate.

Loschner et al. teach using the device for sensor applications.

Lee et al. teach in figure 3 and related text a semiconductor substrate 31 used for sensor applications 30, wherein a selective oxide film 32 (field oxide region) is formed as part of the substrate.

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to form a field oxide region (a selective oxide film) in the thin portion of Loschner et al.'s device in order to provide protection to the active regions when operating the device in its intended use (in sensor applications).

Response to Arguments

Applicant's arguments with respect to claim 1 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ori Nadav whose telephone number is 571-272-1660. The examiner can normally be reached between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Gurley can be reached on 571-272-1670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

O.N. 12/3/07

ORI NADAV
PRIMARY EXAMINER
TECHNOLOGY CENTER 2800